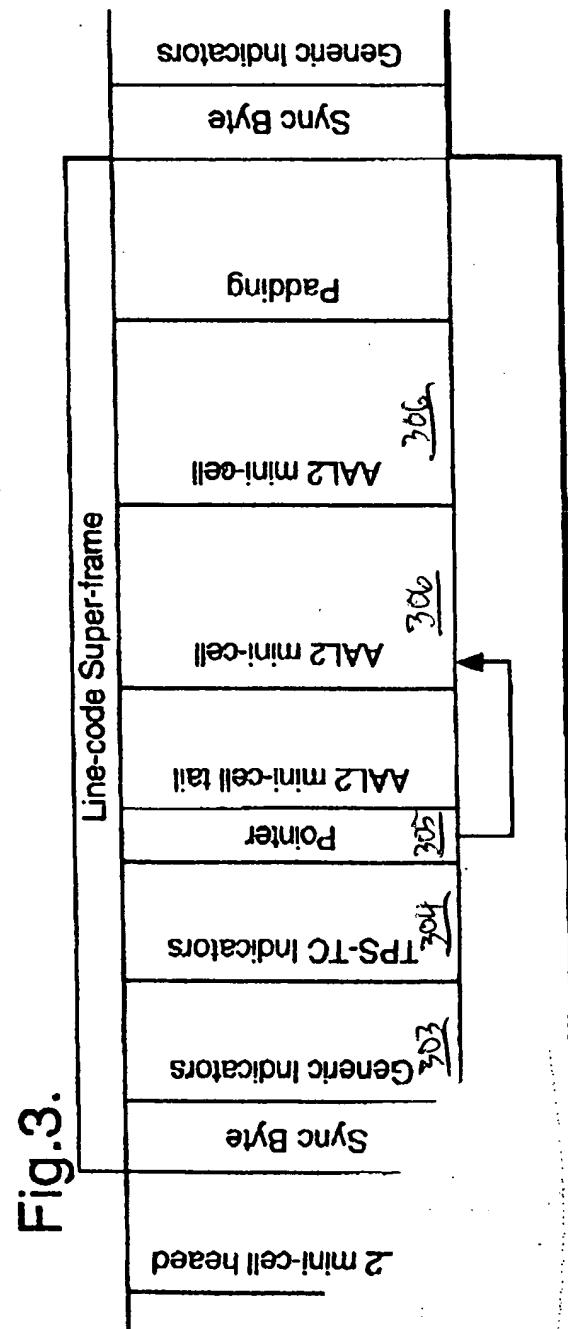
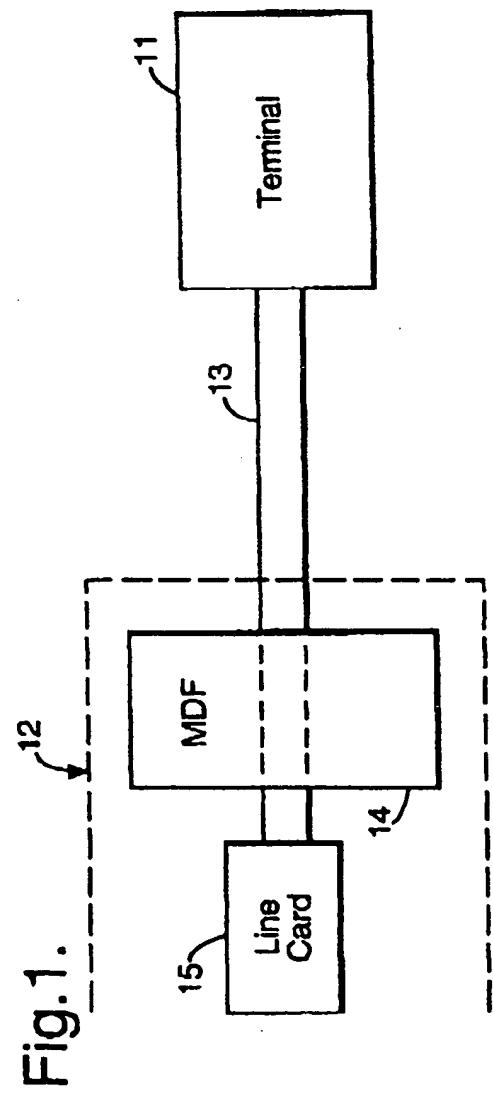


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OK - FIG 1-8
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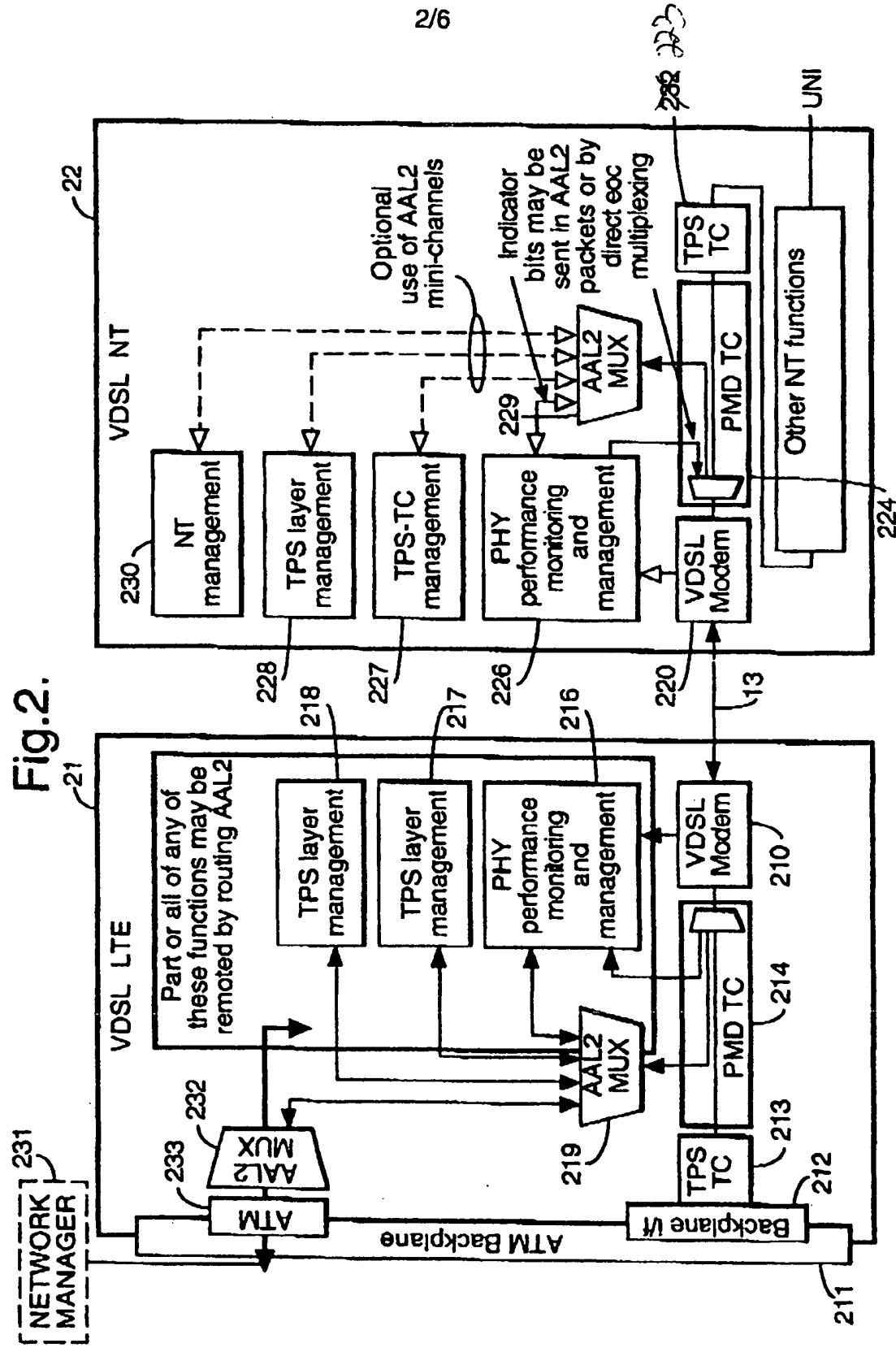
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Fig.2.

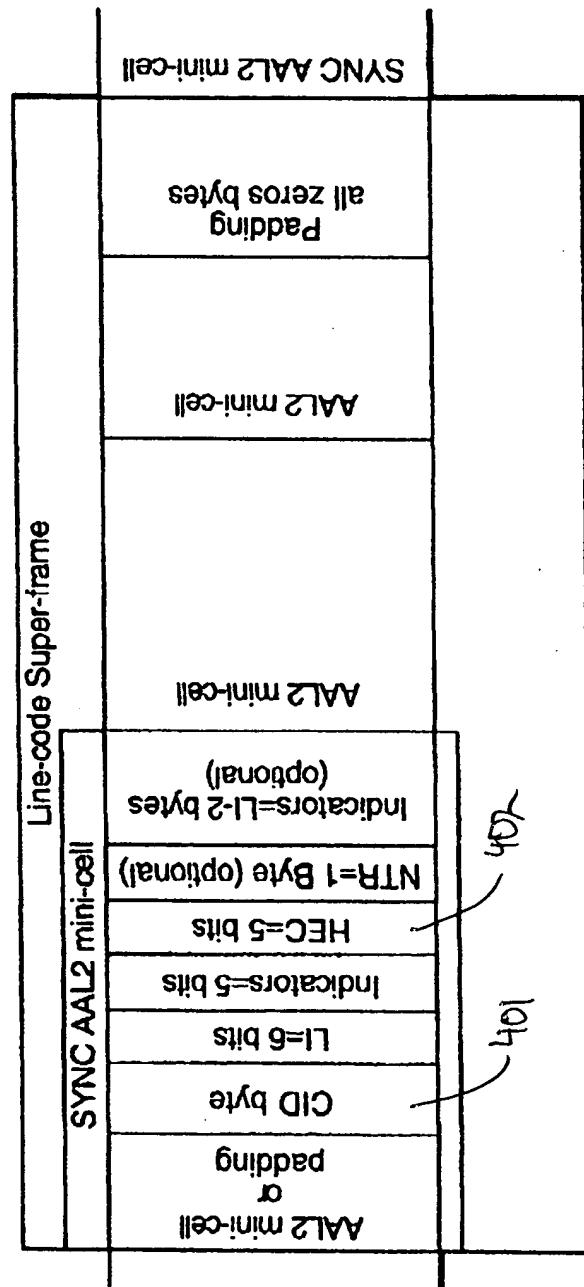


ST AVAILABLE COPY

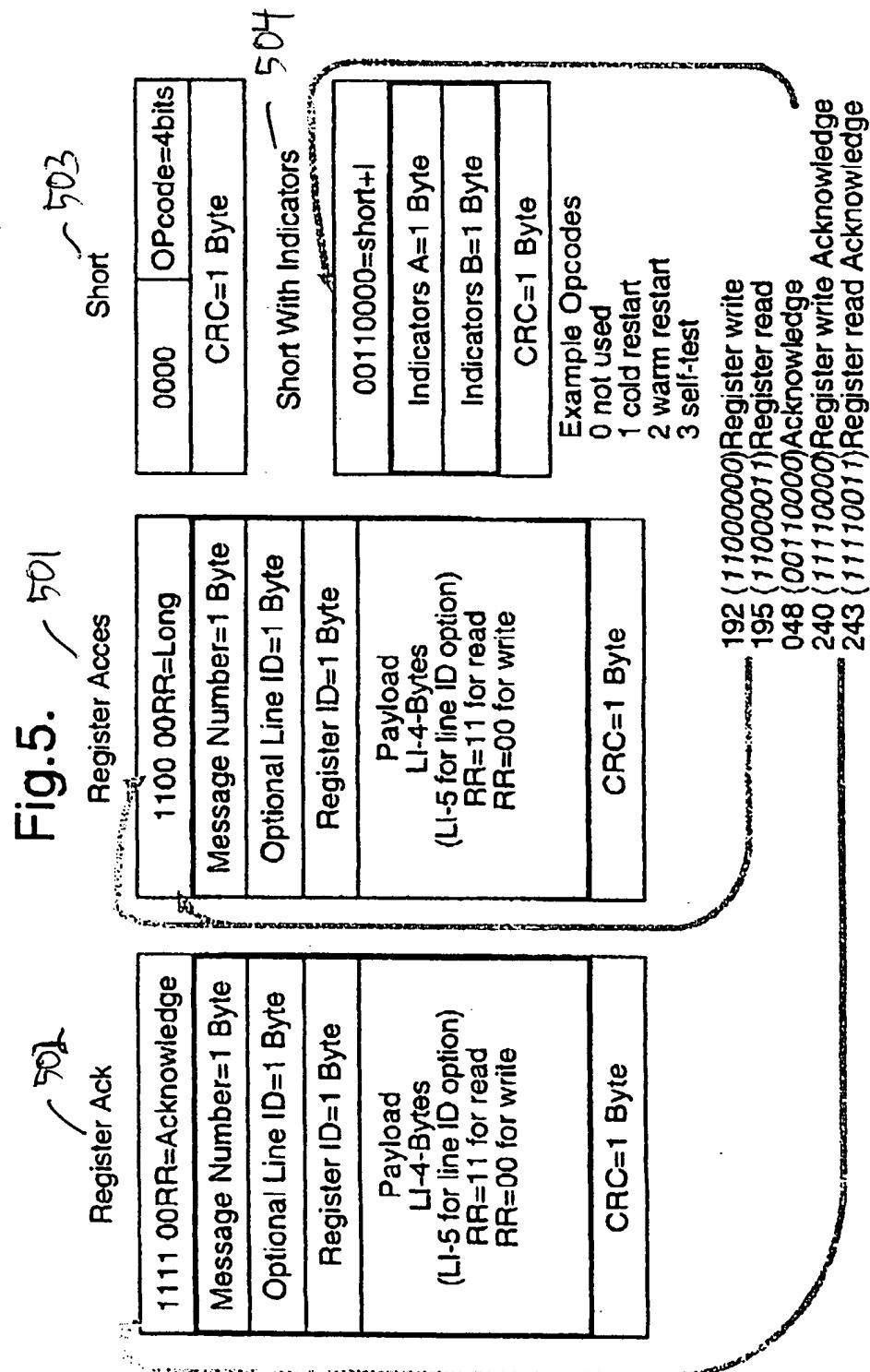
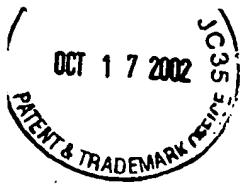


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Fig. 4.



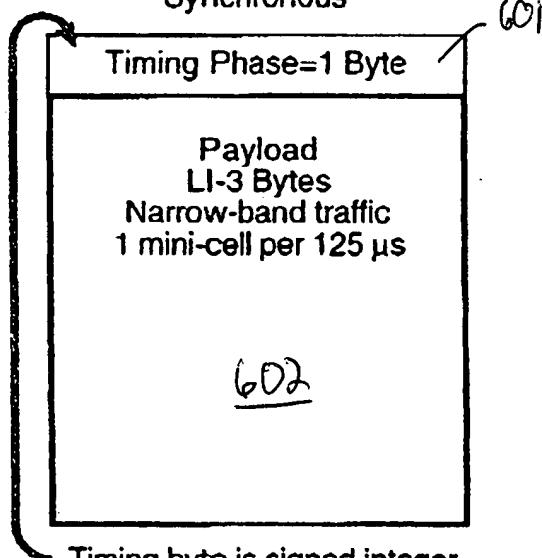
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Fig.6a.

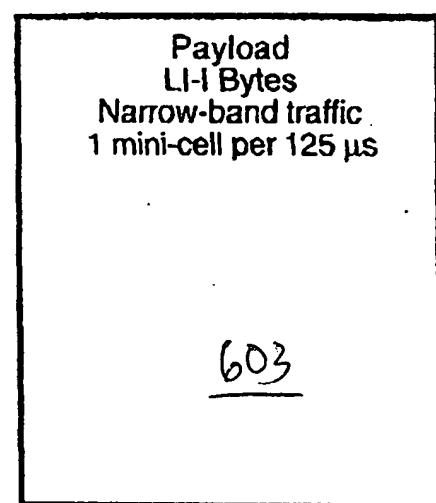
Synchronous



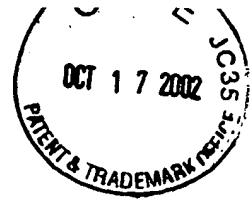
Timing byte is signed integer giving offset from NTR phase

Fig.6b.

Plesiochronous



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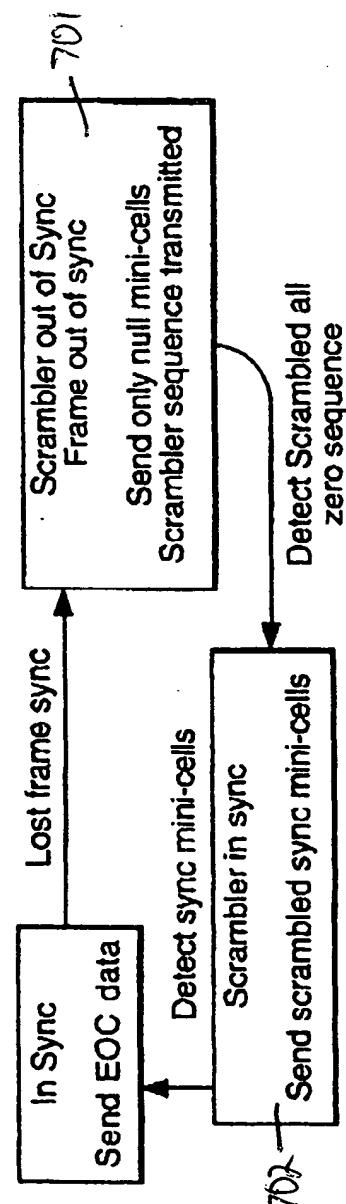
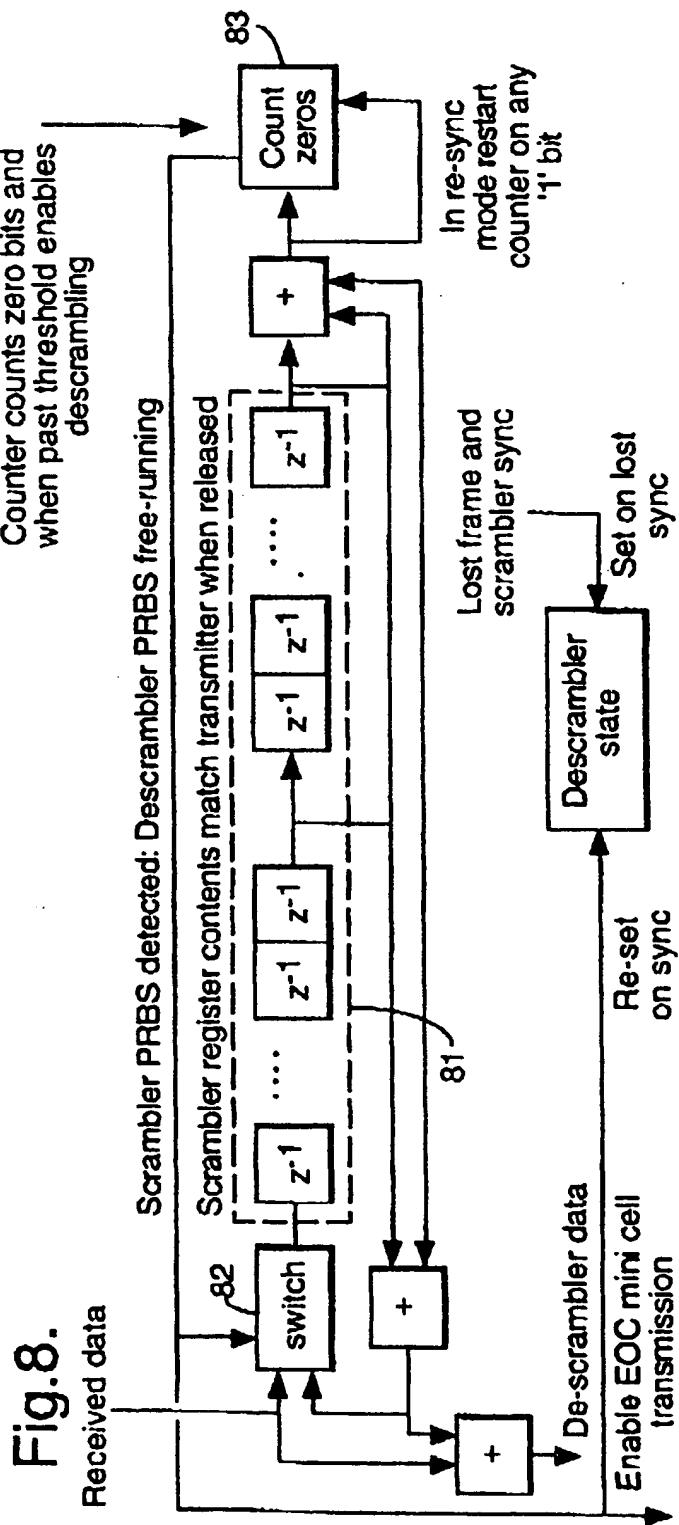


Fig. 7.



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